AC- 29/06/2021
Item No. - 6.14

UNIVERSITY OF MUMBAI

Program: Bachelor of Engineering in Electronics & Computer Science

Second Year with Effect from AY 2020-21
Third Year with Effect from AY 2021-22
Final Year with Effect from AY 2022-23

(REV- 2019 'C' Scheme) from Academic Year 2019 – 20
Under

FACULTY OF SCIENCE & TECHNOLOGY

(As per AICTE guidelines with effect from the academic year 2019–2020)

AC – 29/06/2021

Item No. – 6.14

UNIVERSITY OF MUMBAI

Syllabus for Approval

Sr. No.	Heading	Particulars
1	Title of the Course	**Third Year BE in Electronics & Computer Science**
2	Eligibility for Admission	**Second Year Engineering passed in line with the Ordinance 0.6243**
3	Passing Marks	**40%**
4	Ordinances / Regulations (if any)	**Ordinance 0.6243**
5	No. of Years / Semesters	**8 Semesters**
6	Level	~~Certificate/Diploma~~/UG/~~PG~~ **(Strike out which is not applicable)**
7	Pattern	**Semester**/~~Yearly~~ **(Strike out which is not applicable)**
8	Status	**Revised**/~~New~~ **(Strike out which is not applicable)**
9	To be implemented from Academic Year	**With effect from Academic Year: 2021-2022**

Date: Signature:

Dr. S. K. Ukarande
Associate Dean
Faculty of Science and Technology
University of Mumbai

Dr Anuradha Muzumdar
Dean
Faculty of Science and Technology
University of Mumbai

Preamble

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this, the Faculty of Science and Technology (in particular Engineering), of University of Mumbai, has taken a lead in incorporating the philosophy of outcome based education in the process of curriculum development.

Faculty resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes, understand the depth and approach of the course to be taught, which will enhance learner's learning process. Choice based Credit and grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. Credit assignment for courses is based on 15 weeks teaching learning process. However, content of courses is to be taught in 12-13 weeks and the remaining 2-3 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

There was a concern that the earlier revised curriculum was more focused on providing information and knowledge across various domains of the said program, which led to heavily loading students in terms of direct contact hours. In this regard, faculty of science and technology resolved that to minimize the burden of contact hours, total credits of the entire program will be of 170, wherein focus is not only on providing knowledge but also on building skills, attitude and self learning. Therefore in the present curriculum, skill based laboratories and mini projects are made mandatory across all disciplines of engineering in second and third year of programs, which will definitely facilitate self learning of students. The overall credits and approach of the curriculum proposed in the present revision is in line with the AICTE model curriculum.

The present curriculum will be implemented for Second Year of Engineering from the academic year 2020-21. Subsequently this will be carried forward for Third Year and Final Year Engineering in the academic years 2021-22, 2022-23, respectively.

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Incorporation and implementation of online contents from NPTEL/ SWAYAM Platform

The curriculum revision is mainly focused on knowledge component, skill based activities and project based activities. Self learning opportunities are provided to learners. In the revision process this time, in particular Revised syllabus of ‘C ‘scheme, wherever possible, additional resource links of platforms such as NPTEL, Swayam are appropriately provided. In earlier revisions of the curriculum in the years 2012 and 2016, in Revised scheme ‘A' and ‘B' respectively, efforts were made to use online contents as additional learning materials to enhance learning of students.

In the current revision based on the recommendation of AICTE model curriculum, overall credits are reduced to 171, to provide opportunity of self-learning to learner. Learners are now getting sufficient time for self-learning either through online courses or additional projects for enhancing their knowledge and skill sets.

The Principals/ HOD’s/ Faculties of all the institutes are required to motivate and encourage learners to use additional online resources available on platforms such as NPTEL/ Swayam. Learners can be advised to take up online courses and on successful completion, they are required to submit certification for the same. This will definitely help learners to facilitate their enhanced learning based on their interest.

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Preface

Technical education in the country is undergoing a paradigm shift in current days. Think tank at national level are deliberating on the issues, which are of utmost importance and posed challenge to all the spheres of technical education. Eventually, impact of these developments was visible and as well adopted on bigger scale by almost all universities across the country. These are primarily an adoption of CBCS (Choice base Credit System) and OBE (Outcome based Education) with student centric and learning centric approach. Education sector in the country, as well, facing critical challenges, such as, the quality of graduates, employability, basic skills, ability to take challenges, work ability in the fields, adoption to the situation, leadership qualities, communication skills and ethical behaviour. On other hand, the aspirants for admission to engineering programs are on decline over the years. An overall admission status across the country is almost 50%; posing threat with more than half the vacancies in various colleges and make their survival difficult. In light of these, an All India Council for Technical Education (AICTE), the national regulator, took initiatives and enforced certain policies for betterment, in timely manner. Few of them are highlighted here, these are design of model curriculum for all prevailing streams, mandatory induction program for new entrants, introduction of skill based and inter/cross discipline courses, mandatory industry internships, creation of digital contents, mandate for use of ICT in teaching learning, virtual laboratory and so on.

To keep the pace with these developments in Technical education, it is mandatory for the Institutes & Universities to adopt these initiatives in phased manner, either partially or in toto. Hence, the ongoing curriculum revision process has a crucial role to play. The BoS of Electronics Engineering under the faculty of Science & Technology, under the gamut of Mumbai University has initiated a step towards adoption of these initiatives. We, the members of Electronics Engineering Board of Studies of Mumbai University feel privileged to present the revised version of curriculum for Electronics & Computer Science program to be implemented from academic year 2020-21. Consent was also extended by BoS Computer Science for this curriculum. Some of the highlights of the revision are;

i. Curriculum has been framed with reduced credits and weekly contact hours, thereby providing free slots to the students to brain storm, debate, explore and apply the engineering principles. The leisure provided through this revision shall favour to inculcate innovation and research attitude amongst the students.
ii. New skill based courses have been incorporated in curriculum keeping in view AICTE model curriculum.
iii. Skill based Lab courses have been introduced, which shall change the thought process and enhance the programming skills and logical thinking of the students
iv. Mini-project with assigned credits shall provide an opportunity to work in a group, balancing the group dynamics, develop leadership qualities, facilitate decision making and enhance problem solving ability with focus towards socio-economic development of the country. In addition, it shall be direct application of theoretical knowledge in practice, thereby, nurture learners to become industry ready and enlighten students for Research, Innovation and Entrepreneurship thereby to nurture start-up ecosystem with better means.
v. An usage of ICT through NPTEL/SWAYAM and other Digital initiatives of Govt. of India shall be encouraged, facilitating the students for self-learning and achieve the Graduate Attribute (GA) specified by National Board of accreditation (NBA) i.e. lifelong learning.

Thus, this revision of curriculum aimed at creating deep impact on the teaching learning methodology to be adopted by affiliating Institutes, thereby nurturing the students fraternity in a multifaceted directions and create competent technical manpower with legitimate skills. In time to come, these graduates shall shoulder the responsibilities of proliferation of future technologies and support in a big way for 'Make in India' initiative a reality. In the process, BoS, Electronics Engineering got whole hearted support from all stakeholders including faculty, Heads of department of affiliating institutes, experts faculty who detailed out the course contents, alumni, industry experts and university official providing all procedural support time to time. We put on record their involvement and sincerely thank one and all for contribution and support extended for this noble cause.

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Boards of Studies in Electronics Engineering

Sr. No.	Name	Designation	Sr. No.	Name	Designation
1	Dr. R. N. Awale	Chairman	5	Dr. Rajani Mangala	Member
2	Dr. Jyothi Digge	Member	6	Dr. Vikas Gupta	Member
3	Dr. V. A. Vyawahare	Member	7	Dr. D. J. Pete	Member
4	Dr. Srija Unnikrishnan	Member	8	Dr. Vivek Agarwal	Member

Program Structure for Third Year Electronics Engineering

UNIVERSITY OF MUMBAI

(With Effect from 2021-2022)

Semester V

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		TH	PR	Tut	TH	Pract	Tut	Total
ECC 501	Communication Engineering	3	-	-	3	-	-	**3**
ECC 502	Computer Organization and Architecture	3	-	-	3	-	-	**3**
ECC 503	Software Engineering	3	-	-	3	-	-	**3**
ECC 504	Web Technologies	3	-	-	3	-	-	**3**
ECC DO501	Department Optional (Course - I)	3	-	-	3	-	-	**3**
ECL501	Communication Engineering Lab	-	2			1		**1**
ECL502	Software Engineering and Web Technologies Lab	-	2	-	-	1	-	**1**
ECL503	Department Optional (Course - I) Lab	-	2	-	-	1	-	**1**
ECL504	Professional Communication and Ethics-II	-	4	-	-	2	-	**2**
ECM501	Mini project - 2A	-	4$	-	-	2	-	**2**
Total		**15**	**14**	**-**	**15**	**7**	**-**	**22**

**Theory class; $ indicates workload of learner (Not faculty), for mini-project*

Course Code	Course Name	Examination Scheme							
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)	TW	Pract/ Oral	Total
		Test 1	Test 2	Av					
ECC 501	Communication Engineering	20	20	20	80	03	-	-	**100**
ECC 502	Computer Organization and Architecture	20	20	20	80	03	-	-	**100**
ECC 503	Software Engineering	20	20	20	80	03	-	-	**100**
ECC 504	Web Technologies	20	20	20	80	03	-	-	**100**
ECC DO501	Department Level Optional Course - I	20	20	20	80	03	-	-	**100**
ECL501	Communication Engineering Lab	-	-	-	-	-	25	25	**50**
ECL502	Software Engineering and Web Technologies lab	-	-	-	-	-	25	25	**50**
ECL503	Department Optional Course -I lab	-	-	-	-	-	25	25	**50**
ECL504	Professional Communication and Ethics-II	-	-	-	-	-	25	25	**50**
ECM501	Mini project - 2A						25	25	**50**
Total				**100**	**400**	**-**	**150**	**100**	**750**

Department Level Optional Course - I (DO 501):

1. Software Testing and Quality Assurance	**3.** Information Theory and Coding
2. ASIC Verification	**4.** Sensors and Applications

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC 501	Communication Engineering	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg. of Test 1 and Test 2						
ECC 501	Communication Engineering	20	20	20	80	03	--	--	--	100

Course Pre-requisite:

- ECC 301 Applied Mathematics-III
- ECC 401 Applied Mathematics-IV
- ECC 303 Digital Electronics
- ECC 302 Electronic Devices

Course Objectives:

1. To understand and analyse the need for various analog modulation techniques
2. To analyse the characteristics of the receivers
3. To understand pulse modulation methods
4. To understand the effect of ISI in Baseband transmission of a digital signal
5. To analyse various Digital modulation techniques

Course Outcomes:

After successful completion of the course students will be able to:

1. Analyse various analog modulation methods.
2. Explain various pulse modulation techniques.
3. Evaluate the impact of Inter Symbol Interference in Baseband transmission and methods to mitigate its effect.
4. Compare various Digital modulation methods based on spectral efficiency, Euclidean distance etc
5. Analyse the characteristics of radio receivers

Module No.	Unit No.	Contents	Hrs.
1		**Introduction to Electronic Communication**	04
	1.1	Electromagnetic Spectrum	
	1.2	Block diagram of Analog communication system	
	1.3	Need for modulation	
	1.4	Types of Noise, Signal-to-noise ratio, Noise factor, Noise Figure, Noise Temperature	
2		**Analog Modulation Systems**	12
	2.1	Principle of Amplitude Modulation (AM): Representation of AM wave (Mathematical & Graphical), Frequency spectrum of AM wave, AM Power Distribution, AM for a Complex Modulating Signal	
	2.2	Types of AM: Generation of DSB-SC using diode based balanced modulator, Generation of SSB using phase shift method	
	2.3	Principles of Angle Modulation: Theory of Frequency Modulation (FM) & Phase Modulation (PM) - Basic Concepts, Spectrum Analysis of FM Wave, Noise triangle, Pre-emphasis, De-emphasis	
	2.4	Comparison of AM, FM and PM	
3		**Radio Transmitters and Receivers**	04
	3.1	Radio Transmitters: Block diagram of AM & FM transmitters	
	3.2	Radio receivers: Receiver Characteristics, Superheterodyne Receiver, diode detector, Automatic gain control (AGC), Automatic frequency control (AFC)	
4		**Pulse Modulation**	05
	4.1	Sampling theorem and quantization of signals	
	4.2	Generation and Detection of Pulse Amplitude Modulation (PAM)	
	4.3	Pulse Code Modulation (PCM), and Delta Modulation (DM)	
	4.4	Multiplexing Techniques: Time Division Multiplexing (TDM):T1 carrier system, Frequency Division Multiplexing (FDM)	
5		**Pulse Shaping for Optimum Transmission**	04
	5.1	Line codes and their desirable properties, PSD of digital data	
	5.2	Concept of Inter symbol interference (ISI), Eye diagram: Quality Factor and BER, Nyquist Bandwidth	
	5.3	Types of equalizers: Linear equalizer	
	5.4	Correlative coding: Duo-binary encoding and modified duo-binary encoding	
6		**Digital Modulation Techniques**	10
	6.1	Bandpass digital transmitter and receiver model	
	6.2	Generation, detection, signal space diagram, power spectral density and spectrum efficiency analysis of: Binary Phase Shift Keying (BPSK), Quaternary Phase Shift Keying (QPSK), M-ary PSK, Binary Amplitude Shift Keying (BASK), Quadrature Amplitude Modulation (QAM), Binary Frequency Shift Keying (BFSK), Minimum Shift Keying (MSK).	
		Total	**39**

Text Books:

1. Simon Haykin, "*Communication System*", John Wiley And Sons ,4th Ed

2. Taub Schilling & Saha, "*Principles Of Communication Systems*", Tata Mc-Graw Hill, Third Ed
3. Kennedy and Davis "Electronics Communication System", Tata McGraw Hill
4. T. L. Singal, "Analog and Digital Communication," Tata Mc-Graw Hill, New Delhi, First Edition, 2012.
5. Sklar B, and Ray P. K., "Digital Communication: Fundamentals and Applications," Pearson, Dorling Kindersley (India), Delhi, Second Edition, 2009.

Reference Books:

1. Bernad Sklar,- "Digital communication", Pearson Education, 2nd Ed.
2. Simon Haykin, "Digital communication", John Wiley and sons
3. Wayne Tomasi, "Electronics Communication Systems" Pearson Education, Third Edition, 2001.
4. R P Singh &S. Sapre, "Analog and Digital Communication", Tata McGraw Hill 2nd Ed.
5. Haykin Simon, "Digital Communication Systems," John Wiley and Sons, New Delhi, Fourth Edition, 2014.
6. Proakis & Salehi, "Communication System Engineering", Pearson Education.

Internal Assessment (IA):
Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:
1. Question paper will comprise of 6 questions, each of 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4. Remaining questions will be selected from all the modules

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC 502	Computer Organization and Architecture	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECC 502	Computer Organization and Architecture	20	20	20	80	03	--	--	--	100

Course Pre-requisite:

1. Digital Electronics
2. Fundamental concepts of processing
3. Data structures

Course Objectives:

1. To introduce the learner to the design aspects which can lead to maximized performance of a Computer.
2. To introduce basic concepts and functions of operating systems.
3. To understand the concepts of process synchronization and deadlock.
4. To understand various Memory, I/O and File management techniques
5. To introduce the learner to various concepts related to Parallel Processing
6. To highlight the various architectural enhancements in modern processors.

After successful completion of the course students will be able to:

1. Define the performance metrics of a Computer
2. Explain the design considerations of Processor, Memory and I/O in Computer systems
3. Interpret the objectives and functions of an Operating System
4. Analyze the concept of process management and evaluate performance of process scheduling algorithms
5. Evaluate the advantages and limitations of Parallelism in systems
6. Discuss the various architectural enhancements in modern processors

Module No.	Unit No.	Contents	Hrs.
1		**Introduction to Computer Organization**	02
	1.1	Fundamental Units of a Computer, Basic Measures of Computer Performance - Clock Speed, CPI, MIPs and MFlops	
	1.2	Number Representation methods- Integer and Floating-point	
2		**Processor Organization and Architecture**	05
	2.1	CPU Architecture, Register Organization, Instruction cycle, Instruction Formats	
	2.2	Control Unit Design- Hardwired and Micro-programmed Control: Vertical and Horizontal Micro-Instructions, Nano-programming	
	2.3	Comparison between CISC and RISC architectures	
3		**Memory and I/O Organization**	09
	3.1	Classification of Memories-Primary and Secondary Memories, ROM and RAM, Memory Inter- leaving	
	3.2	Memory Hierarchy, Cache Memory Concepts, Mapping Techniques, Write Policies, Cache Coherency	
	3.3	Virtual Memory Management-Concept, Segmentation, Paging, Page Replacement policies	
	3.4	Types of I/O devices and Access methods, Types of Buses, Bus Arbitration	15
4		**Operating System concepts**	
	4.1	Concept of a Process, Process States, Process Description, Process Control Block	
	4.2	Process scheduling -Pre-emptive and Non pre-emptive scheduling algorithms (FCFS, Priority, SJF), Concept of Multi-Threading	
	4.3	Inter-Process Communication, Process Synchronization, Deadlock and Prevention	
	4.4	File Management -File Organization and Access	
	4.5	I/O Management and Disk Scheduling: FCFS, SSTF	
5		**Parallelism**	04
	5.1	Introduction to Parallel Processing Concepts, Flynn's classification, Amdahl's law	
	5.2	Pipelining - Concept, Speedup, Efficiency, Throughput, Types of Pipeline hazards and solutions	
6		**Architectural Enhancements**	04
		Superscalar Architectures, Out-of-Order Execution, Multi-core processors, Clusters, GPU	
		Total	39

Text Books:

1. William Stallings, "*Computer Organization and Architecture: Designing for Performance*", Eighth Edition, Pearson.
2. C. Hamacher, Z. Vranesic and S. Zaky, "Computer Organization", McGraw Hill,2002.
3. William Stallings, Operating System: Internals and Design Principles, Prentice Hall, 8th Edition
4. Abraham Silberschatz, Peter Baer Galvin and Greg Gagne, Operating System Concepts, John Wiley &Sons, Inc., 9th Edition,

Reference Books:

1. P. Hayes, "Computer Architecture and Organization", McGraw-Hill,1998.
2. B. Govindarajulu, "*Computer Architecture and Organization: Design Principles and Applications*", Second Edition, Tata McGraw-Hill.
3. D. A. Patterson and J. L. Hennessy, "Computer Organization and Design - The Hardware/Software Interface", MorganKaufmann,1998.
4. Achyut Godbole and Atul Kahate, Operating Systems, McGraw Hill Education, 3rd Edition
5. Andrew Tannenbaum, Operating System Design and Implementation, Pearson, 3rd Edition

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each of 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4. Remaining questions will be selected from all the modules

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC 503	Software Engineering	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECC 503	Software Engineering	20	20	20	80	03	--	--	--	100

Course Pre-requisites:

1. Knowledge of Software Application Domains, Software Engineering Practices.
2. Knowledge of any Programming Language

Course Objectives:

1. To learn the basics of software engineering and software development process models, agile software development and other agile practices.
2. To Identify, Specify, analyse Software Requirements and prepare model.
3. To understand concepts and principles of software design and Development.
4. To learn about Project Scheduling concept and Software Cost Estimation Techniques.
5. To understand concept of software quality assurance and Risk Management.
6. To learn different software testing strategies and tactics.

Course Outcomes:
After successful completion of the course Students will be able to:

1. Apply software engineering concept and choose process models for a software project development.
2. Analyse and specify software requirement specification (SRS) for software system.
3. Convert requirement model into the design model and demonstrate the use of software and user-interface design principles.
4. Generate the project schedule and estimate the cost of software system.
5. Identify risks and prepare RMMM plan for quality software system.
6. Apply testing strategies and tactics for software system.

Module No.	Unit No.	Contents	Hrs.
1		**Introduction to Software Engineering and Process Models**	7
	1.1	Nature of Software, Software Process framework	
	1.2	Prescriptive Models: Waterfall Model, Incremental, RAD Models Evolutionary Process Models: Prototyping, Spiral and Concurrent Development Model. Specialized Models: Component based	
	1.3	Agile process, Agility Principles, Extreme Programming (XP), Scrum.	
2		**Requirement Engineering and Modelling**	8
	2.1	Types of Requirements, Requirement Engineering Task, Software Requirement Specification (SRS), Developing Use Cases (UML)	
	2.2	Requirement Model: Scenario-based model, Class-based model, Behavioural model.	
3		**Design Engineering**	6
	3.1	Design Concepts, Design Principles	
	3.2	Architecture Design, Component Level Design, System Level Design, User Interface Design	
4		**Project scheduling & Cost Estimation**	6
	4.1	Project Scheduling, defining a Task Set for the Software Project, Gantt charts, Program Evaluation Review Techniques (PERT), Tracking the Schedule	
	4.2	Software Project Estimation, Decomposition Techniques, LOC based, FP based and Use case-based estimations, Empirical estimation Models. COCOMO II Model.	
5		**Software Risk &Quality Management**	6
	5.1	Software Risk, Types of Risk, Risk Identification, Risk Assessment, Risk Projection, RMMM.	
	5.2	Software Quality Assurance Task and Plan, McCall's Quality Factors, Software Reliability, Formal Technical Review (FTR), Walkthrough	
6		**Software Testing Strategies and Tactics**	6
	6.1	Software Testing Fundamentals, Testing strategies for conventional and Object-Oriented architectures, Unit testing, Integration testing, System Testing, Validation and System Testing.	
	6.2	Testing Tactics: White-Box Testing, Basis Path Testing, Control Structure Testing, Black-Box Testing.	
		Total	**39**

Text Books:

1. Roger S Pressman "Software Engineering: A Practitioner's Approach" 8th Edition McGraw-Hill, ISBN:978-0-07-802212-8
2. Pankaj Jalote, "An integrated approach to Software Engineering", Springer/Narosa

Reference Books:

1. Ian Sommerville, “Software Engineering”, Pearson Education (9th edition)
2. Jibitesh Mishra and Ashok Mohanty, “Software Engineering”, Pearson edition
3. Rajib Mall, "Fundamentals of Software Engineering", Prentice Hall India
4. Hans Van Vilet, “Software Engineering Principles and Practice” 3rd edition Wiley

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each of 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus where in sub questions of 2 to 5 marks will be asked.
4. Remaining questions will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC504	Web Technologies	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECC504	Web Technologies	20	20	20	80	03	--	--	-	100

Course Pre-requisite: Basics of programming languages, basic knowledge of HTML

Course Objectives:

1. To design and create web pages using HTML5 and CSS3.
2. To implement client-side scripting to static web pages.
3. To create dynamic web pages using server-side scripting.
4. To use MVC framework for web application development.

Course Outcomes:

After successful completion of the course students will be able to:

1. Design static web pages using HTML5.
2. Design the layout of web pages using CSS3.
3. Apply the concepts of client-side validation and scripts to static web pages using JavaScript and JQuery.
4. Build responsive web pages using front-end framework Bootstrap.
5. Build dynamic web pages using server -side scripting.
6. Develop a web application using appropriate web development framework.

Module	Unit No.	Contents	Hrs.
1		**Introduction to HTML5**	
	1.1	Basic structure of an HTML5 document, Creating an HTML5 document, Mark up Tags, Heading-Paragraphs, line Breaks HTML5 Tags - Introduction to elements of HTML, Working with Text, Lists, Tables and Frames, Hyperlinks, Images and Multimedia, Forms and other HTML5 controls.	4
	1.2	Self-Learning: HTML5 based game development	
2		**Static Web Page Design**	
	2.1	Concept of CSS, Creating Style Sheet, CSS Properties, CSS Styling (Background, Text Format, Controlling Fonts), Working with block elements and objects, Lists and Tables, CSS Id and Class, Box Model(Introduction, Border properties, Padding Properties, Margin properties) CSS Advanced: (Grouping, Dimension, Display, Positioning, Floating, Align, Pseudo class, Navigation Bar, Image Sprites, Attribute sector)	4
	2.2	Self-Learning: Creating page Layout and Site Designs	
3		**Client-side scripting**	
	3.1	**JavaScript**	
		Introduction to JavaScript, Lexical Structure, Types, Values, Variables, Expressions and Operators, Statements, Objects, Arrays, Functions, Pattern matching with regular expressions, JavaScript in Web Browsers, The Window object, Scripting Documents, Scripting CSS, Handling Events	6
	3.2	**jQuery**	
		jQuery Basics, jQuery Getters and Setters, Altering Document Structure, Handling events with jQuery, Animated Effects, Utility functions, jQuery Selectors and Selection Methods, Extending jQuery with Plug-ins, The jQuery UI Library	4
	3.3	Self-Learning: JavaScript Framework -AngularJS	
4		**Bootstrap**	
	4.1	Introduction to Bootstrap, downloading and installing Bootstrap. **The Grid System**: Introducing the Grid, Offsetting and Nesting, Responsive Features, Utility Classes, and Supported Devices. **CSS Foundations**: Typography in Bootstrap, Styling Tables, Styling Forms, Styling Buttons, Images, icons, and Thumbnails. **Navigation Systems**: Tabs, Pills, and Lists, Breadcrumbs and Pagination, Navigation Bar, Making the Navigation Bar Responsive. **JavaScript Effects**: Drop-downs, Modal Windows, Tooltips and Popovers, Navigation Aids: Tabs, Collapse, Affix, Carousel.	6
	4.2	Self-Learning: Bootstrap Customization: Combining Elements in Bootstrap, Customizing by Components, Plugins, and Variables	
5		**Server side-scripting**	
	5.1	Introduction to PHP, PHP Tags, Adding Dynamic content, Accessing form variables, Identifiers, user-declared variables, Data types, Constants,	10

		Operators, Control structures, Conditionals, Iteration constructs, Using arrays, string manipulation and regular expressions, reusing code and writing functions, Designing and creating your web database, Accessing MySQL database from the Web with PHP, Session Control in PHP.	
	5.2	Self-Learning: PHP-NoSQL Database connectivity e.g. PHP-MongoDB connectivity	
6		**Web Development Framework**	5
	6.1	MVC architecture - Introduction and applications	
		Server side-scripting – Laravel Framework Managing Your Project Controllers, Layout, Views, and Other Assets, Talking to the Database, Model Relations, Scopes, and Other Advanced Features, Integrating Web Forms, Authenticating and Managing Your Users, Deploying, Optimizing and Maintaining Your Application	
	6.2	Self-learning: Django Framework, Interactive web sites, web-based information system, blogs, social networking sites,	
		Total	**39**

Text Books:

1. Ralph Moseley, M.T. Savliya , “Developing Web Applications”, Willy India, Second Edition,
2. “Web Technology Black Book”, Dreamtech Press, First Edition, 978-7722-997
3. Robin Nixon, "Learning PHP, MySQL, JavaScript, CSS & HTML5" Third Edition,O'REILLY,2014.(http://www.ebooksbucket.com/uploads/itprogramming/javascript/Learning_PHP_MySQL_Javascript_CSS_HTML5__Robin_Nixon_3e.pdf)
4. Professional Rich Internet Applications: AJAX and Beyond, Dana Moore, Raymond Budd, Edward Benson, Wiley publications. **https://ebooks-it.org/0470082801-ebook.htm**
5. Jennifer Kyrnin, “SAMS Teach Yourself Bootstrap in 24 hours”, 1st edition, Pearson Education.
6. Martin Bean, “Laravel 5 Essentials”, PACKT Publishing Ltd

Reference Books:

1. Harvey & Paul Deitel& Associates, Harvey Deitel and Abbey Deitel, “Internet and World Wide Web - How To Program”, Fifth Edition, Pearson Education, 2011.
2. Achyut S Godbole and Atul Kahate, “Web Technologies”, Second Edition, Tata McGraw Hill, 2012.
3. Thomas A Powell, Fritz Schneider, “JavaScript: The Complete Reference”, Third Edition, Tata McGraw Hill, 2013.
4. David Flanagan, “JavaScript: The Definitive Guide, Sixth Edition”, O'Reilly Media, 2011
5. Steven Holzner, “The Complete Reference – PHP”, Tata McGraw Hill, 2008
6. Mike Mcgrath, “PHP & MySQL in easy Steps”, Tata McGraw Hill, 2012.
7. J. Millman and A. Grabel, “Head First HTML and CSS”, 2nd edition, O‟ Reilly.
8. Ben Frain, “Responsive Web design with HTML5 and CSS3”, PACKT Publishing Ltd.
9. L. Welling and L. Thomson, “PHP and MySQL Web Development”, 4th edition, Adison Wesley Professional.

Digital Material:

1. www.nptelvideos.in
2. www.w3schools.com
3. http://spoken-tutorial.org

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each of 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4. Remaining questions will be selected from all the modules

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDO501	Software Testing & Quality Assurance	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical/Oral	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECCDO501	Software Testing & Quality Assurance	20	20	20	80	03	--	--	--	100

Course Pre-requisite: Programming Language (C++, Java), Software Engineering

Course Objectives:

1. To provide students with knowledge in Software Testing techniques.
2. To provide knowledge of Black Box and White Box testing techniques.
3. To provide skills to design test case plans for testing software.
4. To prepare test plans and schedules for testing projects.
5. To understand how testing methods can be used in a specialized environment.
6. To understand how testing methods can be used as an effective tool in providing quality assurance concerning software.

Course Outcomes:

After successful completion of the course students will be able to:

1. Investigate the reason for bugs and analyse the principles in software testing to prevent and remove bugs.
2. Understand various software testing methods and strategies.
3. Design test planning.
4. Manage the test process.
5. Apply the software testing techniques in the commercial environment.
6. Use practical knowledge of a variety of ways to test software and quality attributes

Module No.	Unit No.	Contents	Hrs.
1		**Testing Methodology**	8
	1.1	Introduction to Software Testing: Introduction, Goals of Software Testing, Software Testing Definitions, Model for Software Testing, Effective Software Testing vs Exhaustive Software Testing, Software Failure Case Studies	
	1.2	Software Testing Terminology and Methodology: Software Testing Terminology, Software Testing Life Cycle (STLC), Software Testing methodology	
	1.3	Verification and Validation: Verification, Verification requirements, Validation	
2		**Testing Techniques**	9
	2.1	Black Box testing: boundary value analysis, equivalence class testing, state table-based testing, cause-effect graphing based testing, error guessing.	
	2.2	White box Testing Techniques: need, logic coverage criteria, basis path testing, graph matrices, loop testing, data flow testing, mutation testing, Static Testing.	
	2.3	Validation Activities: Unit validation, Integration, Function, System, Acceptance Testing.	
	2.4	Regression Testing: Progressive vs. Regressive	
3		**Managing the Test Process**	7
	3.1	Test Management: test organization, structure and of testing group, test planning, detailed test design and test specification.	
	3.2	Software Metrics: need, definition and classification of software matrices.	
	3.3	Efficient Test Suite Management: minimizing the test suite and its benefits	
4		**Test Automation**	4
	4.1	Automation and Testing Tools: need, categorization, selection and cost in testing tool,	
	4.2	Guidelines for testing tools.	
5		**Testing for specialized environment**	5
	5.1	Agile Testing, Agile Testing Life Cycle, Challenges in Agile Testing	
	5.2	Testing Object-Oriented Software: OOT Basics, Object-oriented Testing	
6		**Quality Management**	6
	6.1	Software Quality Management, McCall's quality factors and Criteria	
	6.2	ISO9000:2000, SIX Sigma	
		Total	**39**

Text Books:

1. Software Testing Principles and Practices, Naresh Chauhan, Oxford Higher Education
2. Software Testing and quality assurance theory and practice, Kshirasagar Naik, Priyadarshi Tripathy, Wiley Publication

Reference Books:

1. Effective Methods for Software Testing, Willam E. Perry, Wiley Publication, third edition
2. Software Testing Concepts and Tools, Nageswara Rao Pusuluri, Dreamtech press

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each of 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on the entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4. Remaining questions will be selected from all the modules

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC DO501	ASIC Verification	03		--	03	--	--	03

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical/ Oral	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECC DO501	ASIC Verification	20	20	20	80	03	--	--	--	100

Course Pre-requisite:

Digital Electronics (ECC 303)

Course Objectives:

1. To introduce the learner System Verilog concepts for verification.
2. To provide understanding of System Verilog and SVA for verification, and understand the improvements in verification efficiency.
3. To introduce the learner advanced verification features such as practical use of classes, randomization, checking and coverage.
4. To highlight the significance of verification in VLSI industry.

Course Outcomes:

After successful completion of the course students will be able to:

1. Demonstrate an understanding of programmable devices and verification methodologies.
2. Exploit new constructs in System Verilog.
3. Summarize ASIC verification techniques such as Randomization, assertions, coverage etc.
4. Create layered test benches for digital designs in system Verilog.
5. Carry out verification of design successfully using simulators.

Module No.	Unit No.	Contents	Hrs.
1		**Programmable Devices and Verification Basics**	7
	1.1	**Programmable Devices**: Different types of Integrated Circuits- CPLD, FPGA, ASIC, SoC (System-on-Chip), SiP (System-in-Package), MCM (Multi-Chip Module), SoP (System-on-Package), Choices based on application and cost, Architecture of FPGA, CPLD (Xilinx and Altera family devices) , Difference between ASIC, FPGA and CPLD, ASIC flow and overview of types of tools used in each stage of lifecycle	
	1.2	**Verification Basics**: Introduction, Verification Process, Verification Plan, Verification Methodology options, Basic Testbench Functionality, Directed Testing, Constrained-Random Stimulus, Functional Coverage, Testbench Components, Layered Testbench, Technology challenges test, Verification languages, Verification IP reuse, Verification approaches.	
2		**Data types, Procedural statements, Connecting the Test bench and Design**	8
	2.1	Data Types: Built-in Data Types, Logic Data type, Fixed-Size Arrays (Packed and Unpacked arrays), Dynamic Arrays, Queues, associative array, array methods – Reduction, Locator & ordering, Creating New Types with typedef, Creating User-Defined Structures, Enumerated Types, Constants, Strings, Expression width.	
	2.2	**Procedural statements:** Procedural Statements, Tasks, Functions, and Void Functions, routine arguments, returning from a routine, Time values.	
	2.3	**Connecting the Test bench and Design:** Separating the test-bench and design, The Interface construct, Grouping Signals in an Interface using Modports, Creating Interface Monitor, Stimulus timing with Clocking Block, Test-bench design Race Condition, Program Block, Connecting it all together, Top level Scope, Program-Module interactions.	
3		**Basic Object -Oriented Programming**	6
	3.1	**OOP**: Class, Creating new objects, Where to Define a Class, OOP Terminology, Understanding Dynamic objects, Object Deallocation, using objects, Static vs Global Variables, Class methods, Defining methods outside class, Scoping rules, Using one class inside another, Understanding Dynamic objects, Copying objects, public vs. local, Building a test-bench	
4		**Randomization and Inter-process Communication**	7
	4.1	**Randomization:** Randomization in system Verilog, Constraint details, Solution probabilities, Controlling multiple constraint blocks, Valid constraints, In-line constraints, The pre-randomize and post-randomize functions, Random number functions, Constraints tips and techniques.	
	4.2	**Threads and Inter-process Communication:** Working with threads, disabling threads, inter-process communication, Events, Semaphores, Mailboxes, building a test-bench with threads and IPC.	
5		**System Verilog Assertions and Functional Coverage**	7
	5.1	**System Verilog Assertions**: Types of Assertions and examples, Immediate	

		Assertions, Concurrent Assertions, SVA Property and Sequences, Implication (Overlapped & Non-Overlapped) Operator and Repetition Operator, System Verilog Assertion built-in methods ($rose, $fell, $stable, $past)	
	5.2	**Functional Coverage**: Coverage Types, Functional Coverage Strategies, Simple Functional Coverage Example, anatomy of a cover group, triggering a cover group, data sampling, cross coverage, generic cover groups, Coverage Options, Parameterized Cover Groups, Analysing Coverage Data, Measuring Coverage Statistics During Simulation.	
6		**System Verilog Test-bench Case studies**	4
	6.1	A complete System Verilog Layered Test-Bench for the simple design of ADDER and Memory module- Test-Bench Architecture, Transaction Class, Generator Class, Interface, Driver Class, Monitor, Scoreboard, Environment, Test, Test Bench Top	
		Total	**39**

Text Books:

1. Chris Spear, “System Verilog for Verification: A guide to learning the testbench language features”, Springer, 3rd Edition.
2. Janick Bergeron, “Writing Testbenches Using System Verilog”, Springer 2006.
3. Stuart Sutherland, Simon Davidmann, and Peter Flake, “System Verilog for Design: A guide to using system verilog for hardware design and modeling”, Springer, 2nd Edition.

Reference Books:

1. Ben Cohen, Srinivasan Venkataramanan, Ajeetha Kumari and Lisa Piper, “System Verilog Assertions Handbook”, Vhdl Cohen Publishing, 3rd edition
2. S Prakash Rashinkar, Peter Paterson and Leena Singh, “System on Chip Verification Methodologies and Techniques”, Kluwer Academic, 1st Edition.
3. System Verilog Language Reference manual
4. Samir Palnitkar, ”Verilog HDL: A guide to Digital Design and Synthesis” second edition, Pearson – IEEE 1364-2001 compliant.
5. Spartan and Virtex family user manuals from Xilinx
6. Verilog Language Reference manual

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each of 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4. Remaining questions will be selected from all the modules

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC DO501	Information Theory and Coding	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECC DO501	Information Theory and Coding	20	20	20	80	03	--	--	--	100

Course Pre-requisite:

Engineering Mathematics - IV **ECC 401**

Course Objectives:

1. To learn the principles and applications of information theory in communication systems.
2. To study various data compression methods.
3. To model the continuous and discrete communication channels.
4. To understand the theoretical framework upon which error-control codes are designed.

Course Outcomes:

After successful completion of the course students will be able to:

1. Comprehend the significance of this quantitative measure of information in the communication systems.
2. Explain entropy, joint entropy, relative entropy, conditional entropy, and channel capacity of a system.
3. Obtain knowledge in designing various source codes and channel codes.
4. Differentiate between lossy and lossless compression techniques.
5. Analyze an efficient data compression scheme for a given information source.
6. Apply the concepts of multimedia communication.

Module No.	Unit No.	Contents	Hrs.
1		**Introduction to Information Theory**	**07**
	1.1	Introduction to Probability theory: Axiomatic definition of probability, Bayes Theorem.	

	1.2	One random variable: Types of random variable, Discrete & Continuous, PMF, PDF and Cumulative distribution Function, Conditional Probability, Independent Event.	
	1.3	Two Random Variable: Discrete and Continuous, Joint probability density function, Joint Distribution function, Marginal probabilities, joint conditional probability.	
	1.4	Concept of amount of information, information units, Entropy: marginal, conditional, joint and relative entropies.	
	1.5	Relation among entropies Mutual information, information rate.	
2		**Source Coding Techniques**	**06**
	2.1	Block Diagram of Digital Communication system.	
	2.2	Encoding techniques, Purpose of encoding, Instantaneous codes, Construction of instantaneous codes, Kraft's inequality, Coding efficiency and redundancy	
	2.3	Source coding theorem. Construction of basic source codes: Shannon Fano coding.	
	2.4	Huffman codes, Extended Huffman coding, Arithmetic Coding, Lempel - Ziv Algorithm-LZW	
3		**Information Channels**	**06**
	3.1	Information Channels: Communication Channels	
	3.2	Channel Models, Channel Matrix, Joint probability Matrix, Binary Symmetric Channel, System Entropies, Mutual Information, Channel Capacity	
	3.3	Discrete Memoryless channels: Binary Symmetric Channel (BSC), Channel Capacity of BSC, redundancy and efficiency of channels.	
	3.4	Channel Capacity: Hartley – Shannon law.	
4		**Codes for error detection and correction**	**08**
	4.1	Parity check coding, Linear block codes, Error detecting and correcting capabilities	
	4.2	Generator and Parity check matrices, Standard array and Syndrome decoding, Hamming codes.	
	4.3	Cyclic codes: Generator polynomial, Generator and Parity check matrices, Encoding of cyclic codes.	
	4.4	Syndrome computation and error detection, Decoding of cyclic codes.	
5		**Convolution Codes**	**06**
	5.1	Encoding and State, Tree and Trellis diagrams.	
	5.2	Maximum likelihood decoding of convolution codes, Viterbi algorithm, Sequential decoding -Stack algorithm.	
	5.3	Interleaving techniques: Block and convolution interleaving.	
6		**Audio and Video Coding**	**06**
	6.1	Linear Predictive coding, code excited LPC, Perceptual coding, MPEG audio coders, Dolby audio coders.	
	6.2	Video compression: Principles, Introduction to H.265& MPEG-4 Part 10 Video standards.	
		Total	**39**

Text Books:

1. Simon Haykin, *Communication Systems*, 4th Edition, John Wiley and Sons.
2. Ranjan Bose, *Information theory, coding and cryptography*, 2nd Edition,Tata McGraw-Hill.
3. R. Togneri, C.J.S deSilva, *Fundamentals of Information Theory and Coding Design*, 1st Edition,Taylor and Francis.
4. Fred Halsall, *Multimedia Communications, Applications Networks Protocols and Standards*, Pearson Education, 1st Edition, Asia.

Reference Books:

1. Bernard Sklar, *Digital Communications Fundamentals and Applications*,2nd Edition, Person Education Asia.
2. Taub and Schilling, *Principles of Communication Systems* , 2nd Edition, Tata McGraw-Hill.
3. Glover and Grant, *Digital Communication*, 2ndEdition, Pearson.
4. T. M. Cover, J. A. Thomas, *Elements of Information Theory*, 2nd Edition,Wiley.
5. Mark Nelson, *Data Compression Book*, 2nd Edition, BPB Publication.
6. Watkinson J, *Compression in Video and Audio*, 1st Edition, Focal Press, London.
7. R. J. McEliece, *The Theory of Information and Coding*, 1st Edition, Cambridge University Press.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each of 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.3.
4. Remaining questions will be selected from all the modules

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC DO501	Sensors and Applications	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECC DO501	Sensors and Applications	20	20	20	80	03	--	--	--	100

Course Pre-requisite:

1. Concept of internal characteristics of passive elements like resistor, capacitor, inductor etc.,
2. Diode and transistor
3. Working, knowledge of basic fundamentals of mechanical terms like position, strain, stress etc

Course Objectives:

1. To understand the stages of product (hardware / software) design & development
2. To learn different considerations of analog, digital & mixed circuit design
3. To be acquainted with methods of PCB design & different tools used for the same
4. To be aware of the importance of testing in product design cycle
5. To gain knowledge about various processes & importance of documentation

Course Outcomes:

After successful completion of the course students will be able to:

1. Understand the concept of sensors and its characteristics
2. Understand the practical approach in design of technology based on different sensors
3. Learn various sensor materials and technology used in designing sensors
4. Implement a prototype for demonstrating the application of the sensors
5. Demonstrate problem solving & troubleshooting skills in sensor applications

Module No.	Unit No.	Contents	Hrs.
1		**Sensors Fundamentals and Characteristics Sensors, Signals and Systems**	06
	1.1	Sensor Classification–Physical, Mechanical, Electrical, Chemical, electro-chemical	
	1.2	Functional unit of sensor: receptor and transducer; Units of Measurements	

	1.3	Sensor Characteristics, Physical Principles of Sensing Electric Charges, Fields, and Potentials; Capacitance; Magnetism; Induction; Resistance; Piezoelectric Effect; Hall Effect; Temperature and Thermal Properties of Material; Heat Transfer; Light; Dynamic Models of Sensor Elements	
2		**Interface Electronic Circuits**	06
	2.1	Input Characteristics of Interface Circuits, Amplifiers, Excitation Circuits	
	2.2	Analog to Digital Converters, Direct Digitization and Processing, Bridge Circuits, Data Transmission, Batteries for Low Power Sensors	
	2.3	Analog and digital filtering	
3		**Sensors in Different Applications**	08
	3.1	Area Occupancy and Motion Detectors; Position, Displacement, and Level; Velocity and Acceleration; Force, Strain, and Tactile Sensors; Pressure Sensors	
	3.2	Temperature Sensors; Biosensors, Gas sensors, proximity sensor. (Correlation of output with the parameter being measured in engineering terms): Only Working principle of each type of sensors and transduction action (for example: detection of change in temperature and conversion to electrical quantity say resistance and corresponding correlation)	
	3.3	Case study of Applications of sensors in Automotive, Manufacturing plants, digital devices such as mobile phone, house-hold instrument such as washing machine (name of various sensors and their usability in each of these applications).	
4		**Sensor Materials and Technologies**	07
	4.1	MEMS-cantilever based sensors and their types such as, accelerometer, gyroscopes: Structure, material used (polysilicon, Silicon etc), working principle, applications.	
	4.2	Metal oxide semiconductor (nano-particles) based sensors such as gas sensors, biomedical sensors, chemical sensors (Structure, material used, working principle, applications)	
5		**Smart Sensors**	06
	5.1	4-20 mA Current Loop	
	5.2	Types of smart Sensors, Limitations of single sensor and applicability of Array-based sensor technology, Electronic-Nose sensors	
	5.3	HART, Industrial buses such as Profibus, CANbus, etc.	
6		**Industrial standards for the sensors and its calibration**	06
	6.1	Basic knowledge about IEC 60601-1-1: Medical Electrical Equipment – Part 1-1, ISA S82.01, NEMA standards	
	6.2	PCI 6.5 to SOX compliance, HIPAA compliance, and FISMA compliance in software development: Basic introduction about each of these standards, Calibration and compatibility	
		Total	**39**

Text Books:

1. Jacob Fraden,Handbook of Modern Sensors Physics, Designs, and Applications, Fourth Edition, Springer
2. D. Patranabis, Sensors and Transducers, 2nd Edition, PHI Publication, New Delhi

3. Mechatronics- Ganesh S. Hegde, Published by University Science Press, 2nd Edition, An imprint of Laxmi Publication Private Limited
4. Terry Bartelt, Process Control Systems and Instrumentation, Delmar Cengage Learning India Edition New edition

Reference Books:

1. www.nptel.ac.in
2. G. Eranna , Metal Oxide Nanostructures as Gas Sensing Devices, Publisher: CRC Press
3. ISA S82.01 - Safety Standard for Electrical & Electronic Test, Measuring, Controlling Related Equipment
4. http://www.ebme.co.uk/arts/safety/part6.htm

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each of 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.3.
4. Remaining questions will be selected from all the modules

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL 501	Communication Engineering Lab	--	02	--	--	01	--	01

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECL 501	Communication Engineering Lab	--	--	--	--	--	25	--	25	50

Laboratory Outcomes:

After successful completion of the course students will be able to:

1. Perform hardware implementation of various analog and digital modulation methods.
2. Illustrate generation and detection of various pulse modulation techniques.
3. Apply techniques to insert Inter Symbol Interference and methods to mitigate its effect.
4. Simulate various analog and digital modulation methods.
5. Demonstrate multiplexing and de-multiplexing of signals using multiplexing techniques.
6. Illustrate the effect of sampling frequency on the reconstructed signal.

Term Work:

At least 10 experiments covering entire syllabus should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Oral exam will be based on the entire syllabus.

Suggested List of Experiments

Sr. No.	Experiments based on laboratory setups
1	Analog Modulation and demodulation: AM
2	Analog Modulation and demodulation: FM

3	Pre-emphasis & De-emphasis
4	Analog Pulse modulation (PAM/PWM/PPM)
5	Time division multiplexing
6	Frequency division multiplexing
7	Verification of Sampling theorem
8	Generation of Line codes
9	Binary modulation and demodulation of BASK
10	Binary modulation and demodulation of BPSK
11	Binary modulation and demodulation of BFSK
	Simulation-based experiments
12	Simulation of AM and FM
13	Simulation of PAM, PPM, PWM
14	Simulation of BPSK/BASK/MSK modulation
15	Simulation of duobinary encoder, decoder

Note: Suggested List of Experiments is indicative. However, flexibilities lie with individual course instructor to design and introduce new, innovative and challenging experiments, (limited to maximum 30% variation to the suggested list) from within the curriculum, so that, the fundamentals and applications can be explored to give greater clarity to the students and they can be motivated to think differently.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL502	Software Engineering and Web Technologies Lab	--	02	--	--	01	--	01

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECL502	Software Engineering and Web Technologies Lab	--	--	--	--	--	25	--	25	50

Laboratory Outcomes:

After successful completion of the course students will be able to:

1. Identify requirements and apply process model to selected case study.
2. Analyse and design models for the selected case study using UML modelling
3. Use various Software Engineering and Project Management Tools
4. Design static web pages using HTML5, CSS3, Bootstrap.
5. Apply the concepts of Client-side validation and scripts to static web pages using JavaScript and JQuery.
6. Build dynamic web pages using Server-Side Scripting.

Term Work:

At least 10 experiments covering entire syllabus of **Software Engineering and Web Technologies (50% Software Engineering and the remaining 50% Web Technologies)** should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus.

Suggested List of Experiments

Sr. No.	Experiment Name
	Software Engineering
1	Prepare detailed statement of problem with feasibility study and identify suitable process model for the same with justification. *
2	Develop Software Requirement Specification (SRS) document in IEEE format for the project. *
3	Prepare schedule for the project using any project management tool *
4	Prepare RMMM plan for the project.
5	Identify scenarios & develop UML Use case and Class Diagram for the project. *
6	Develop Activity / State Transition diagram and Sequence diagram for the project. *
7	Develop test cases for the project using white box testing.
	Web Technologies
1	a) Installation and Setting of LAMP / WAMP / XAMP.
	b) Develop a Prototype of the selected problem statement (UI and UX).
2	Design and Implement web pages using HTML5 and CSS3 on the selected problem statement.
3	Design Form using javascript/HTML/JQuery with client-side validations on the selected problem statement.
4	Design Interactive web pages using PHP (any framework) with database connectivity to MySQL on the selected problem statement.
5	Design and Implement web pages with PHP and Ajax on the selected problem statement.
6	Enhance the web page designed in experiment number 2 using bootstrap.
• *Practicals (Software Engineering) can be conducted using any open-source software tools like **Dia, Star UML, Project Libre** etc.* • *Students are expected to pick up one **Case study/Mini Project** such as hospital management, student management, e-shop etc., and perform all the experiments based on that.*	

Text Books:

1. "The Unified Modelling Language User Guide" by Grady Booch, James Rumbaugh, Ivar Jacobson, Pearson Publication, ISBN 978-81-7758-372-4

References:

1. UML – Tutorial "www.tutorialspoints.com/uml/"
2. "Fundamentals of Object-Oriented Design in UML", Meilir Page-Jones, Pearson Education
3. UML Basics— an Introduction to the Unified Modeling Language – IBM "www.ibm.com › Learn › Rational"
4. UML in 24 Hours

Note: Suggested List of Experiments is indicative. However, flexibilities lie with individual course instructor to design and introduce new, innovative and challenging experiments, (limited to maximum 30% variation to the suggested list) from within the curriculum, so that, the fundamentals and applications can be explored to give greater clarity to the students and they can be motivated to think differently.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL 503	Software Testing & Quality Assurance Lab	--	02	--	--	01	--	01

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECL 503	Software Testing & Quality Assurance Lab	--	--	--	--	--	25	--	25	50

Laboratory Outcomes:

After successful completion of the laboratory, students will be able to:

1. Understand the system thoroughly (for requirement, designing and implementation).
2. Recognize failures in the system.
3. Investigate the reason for bugs.
4. Design test plan and test cases.
5. Execute the test cases manually and using automated tools.
6. Manage the testing process.

Term Work:

At least 10 experiments covering entire syllabus should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus.

Suggested List of Experiments

Sr. No.	Experiment Name
1	Write programs in C Language to demonstrate the working of the following a. constructs: i) do...while ii) while....do iii) if...else iv)switch

2	Write a program for any one function of the selected system. Introspect the causes for its failure and write down the possible reasons for its failure.
3	Study the system, requirement specifications and Designing the system.
4	Write the brief test plan.
5	Select the test cases(positive and negative scenarios) for the selected system.
6	Design Test cases for the system using boundary value analysis or equivalent class partitioning.
7	Manual execution of test cases and prepare defect reports.
8	Identify regression scenarios for automation for any one/two test case.
9	Study of any testing tool (e.g. Selenium).
10	Automate the scenario in exp 8 with a testing tool. (e.g. Selenium)
11	Study of any test management tool (e.g. Qase).
12	Writing down test cases and execution using tools (e.g. Qase).
13	Study defect management (e.g. JIRA)
14	Design quality matrix for your system.
Consider one system (e.g. Library Management System, ATM system, Banking application, Library Management System) and use throughout the lab.	

Note: Suggested List of Experiments is indicative. However, flexibilities lie with individual course instructor to design and introduce new, innovative and challenging experiments, (limited to maximum 30% variation to the suggested list) from within the curriculum, so that, the fundamentals and applications can be explored to give greater clarity to the students and they can be motivated to think differently.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL 503	ASIC Verification	--	02	--	--	01	--	01

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	ractical/ Oral	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECL 503	ASIC Verification	--	--	--	--	--	25	25	--	50

Laboratory Outcomes:

After successful completion of the laboratory students will be able to;

1. Create test plan and test cases to verify any digital design.
2. Apply the advanced verification techniques like Randomization on set of inputs.
3. Create a transaction class and apply object -oriented programming for Verification.
4. Carry out simulation of designs using System Verilog hardware verification language.
5. Develop a complete Layered Test-bench for any digital design.

Term Work:

At least 10 experiments covering entire syllabus should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus.

Suggested List of Experiments

Sr. No.	Experiment Name
1	Write Verilog code for 4:1 MUX using all Verilog modeling styles and simulate the same.
2	Write Verilog code and test-bench for D flip flop and 4 bit counter and simulate the same.
3	Create a test plan and self-checking test-bench for the ALU.
4	Create dynamic arrays, associative arrays, and queues using System Verilog.

5	Write test bench using dynamic arrays, associative arrays with System Verilog to test a synchronous 8-bit x64K (512kBit) RAM.
6	Create an Interface for a Memory Design. Use Modport to assign direction to signal.
7	Create class and its objects and perform deep copy and shallow copy.
8	Create an Interface for a Memory Design. (without modport)
9	To understand and create Virtual interface and use it in a class.
10	Given design specifications, draw waveform and write SVA expressions.
11	Given design specifications, draw waveform and write clock based Sequences
12	Create IPCs like events, mailbox and semaphores to interact between threads.
13	Find coverage by writing cover groups for a design.
14	Implementation of parallel processes using Fork Join/ join_any/ join_none statement.
15	Create a layered test-bench for a simple design like Adder.

Note: Suggested List of Experiments is indicative. However, flexibilities lie with individual course instructor to design and introduce new, innovative and challenging experiments, (limited to maximum 30% variation to the suggested list) from within the curriculum, so that, the fundamentals and applications can be explored to give greater clarity to the students and they can be motivated to think differently.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL 503	Information Theory and Coding	--	02	--	--	01	--	01

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical/ Oral	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECL 503	Information Theory and Coding	--	--	--	--	--	25	25	--	50

Laboratory Outcomes:

After successful completion of the laboratory students will be able to

1. Understand the basics of information theory, source coding techniques and calculate Entropy of source.
2. Implement Shannon-Hartley equation to find the upper limit on the Channel Capacity.
3. Apply various channel coding schemes & demonstrate their capabilities towards the improvement of the noise performance of communication system.
4. Apply the knowledge of digital electronics and describe the error control codes like block code, cyclic code and convolutional codes.
5. Implement audio and video compression techniques

Term Work:

At least 10 experiments covering entire syllabus of **Information Theory and Coding (ECC DO501)** should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus.

Suggested List of Experiments

Sr. No.	Experiment Name
1	Write a program for determination of entropy and mutual information of a given channel: Noise free channel.

2	Write a program for determination of entropy and mutual information of a given channel: Binary symmetric channel.
3	Write a program for Shannon-Hartley equation to find the upper limit on the Channel Capacity
4	Write a program for generation and evaluation of variable length source coding Shannon – Fano Coding and decoding.
5	Write a program for generation and evaluation of variable length source coding Huffman Coding and decoding.
6	Write a program for generation and evaluation of variable length source coding LZW Coding and decoding.
7	Write a program for Forward error correction system with a given Linear block code.
8	Write a Program for coding & decoding of Linear block codes.
9	Write a Program for coding & decoding of Cyclic codes.
10	Write a program for coding and decoding of Convolutional codes.
11	Write a program for computing the LPC coefficients.
12	Write a program for video compression.

Note: Suggested List of Experiments is indicative. However, flexibilities lie with individual course instructor to design and introduce new, innovative and challenging experiments, (limited to maximum 30% variation to the suggested list) from within the curriculum, so that, the fundamentals and applications can be explored to give greater clarity to the students and they can be motivated to think differently.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL503	Sensors and Applications	--	02	--	--	01	--	01

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical/ Oral	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECL503	Sensors and Applications	--	--	--	--	--	25	25	--	50

Laboratory Outcomes:
After successful completion of the laboratory students will be able to

1. Choose proper sensor with its thorough understanding of the characteristics.
2. Design suitable signal conditioning circuit for the chosen sensors
3. Perform characterization of sensor materials and technology used in different sensors
4. Implement a prototype for demonstrating the application of the sensors
5. Demonstrate problem solving & troubleshooting skills in sensor applications

Term Work:

At least 10 experiments covering entire syllabus of **Sensors and Applications** should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus.

Suggested List of Experiments

Sr. No.	Experiment Name
1	Characteristics of temperature sensors
2	Characteristics of optical Sensors
3	I to V and V to I converter
4	Frequency to voltage converter using OpAmp

5	Inverting and non-inverting amplifier using OpAmp
6	LVDT Sensor construction and characteristics
7	Instrumentation Amplifier Design
8	Filter Design (Analog)
9	Filter Design (Digital Simulation)
10	Case study on any house hold appliance
11	4-20mA Current Loop
12	Interface with Real word using A/D converters
13	Simulations of Micro-sensors
14	Simulations of micro-actuators such as micro-heater/ micro-motors

Case study: Make a detailed report on industrial applications of sensor: Automotive, mobile phone, consumer products or household equipment such as fridge, washing machine (anyone, all students in a batch should take up different problem statement). The case study should include:

1. Name of equipment
2. Application of selected equipment
3. Sensors used in that equipment, working principle of each type of sensor
4. Draw the complete block diagram of equipment and explain the working of each block.
5. Summary
6. References

References:

1. https://www.microchip.com/stellent/groups/sitecomm_sg/documents/devicedoc/en542976.pdf
2. Practical Design Techniques for Sensor Signal Conditioning, 1999, Edited by Walt Kester, Analog Devices, 1999, ISBN-0-916550-20-6 https://www.analog.com/en/education/education-library/practical-design-techniques-sensor-signal-conditioning.html#

Note: Suggested List of Experiments is indicative. However, flexibilities lie with individual course instructor to design and introduce new, innovative and challenging experiments, (limited to maximum 30% variation to the suggested list) from within the curriculum, so that, the fundamentals and applications can be explored to give greater clarity to the students and they can be motivated to think differently.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL504	Professional Communication and Ethics-II	--	2*+ 2 Hours (Batch-wise)	--	--	02	--	02

Theory class to be conducted for full class.

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ECL504	Professional Communication and Ethics - II	--	--	--	--	--	25	--	25	50

Course Objectives:

Learners should be able to:

1. Discern and develop an effective style of writing important technical/business documents.
2. Investigate possible resources and plan a successful job campaign.
3. Understand the dynamics of professional communication in the form of group discussions, meetings, etc. required for career enhancement.
4. Develop creative and impactful presentation skills.
5. Analyse personal traits, interests, values, aptitude and skills.
6. Understand the importance of integrity and develop a personal code of ethics

Course Outcomes:

After successful completion of the course students will be able to:

1. Plan and prepare effective business/ technical documents which will in turn provide solid foundation for their future managerial roles.
2. Strategize their personal and professional skills to build a professional image and meet the demands of the industry.
3. Emerge successful in group discussions, meetings and result-oriented agreeable solutions in group communication situations.
4. Deliver persuasive and professional presentations.
5. Develop creative thinking and interpersonal skills required for effective professional communication.
6. Apply codes of ethical conduct, personal integrity and norms of organizational behavior.

Module No.	Unit No.	Contents	Hrs.
1		**ADVANCED TECHNICAL WRITING: PROJECT/PROBLEM BASED LEARNING (PBL)**	06
	1.1	**Purpose and Classification of Reports** Classification on the basis of: Subject Matter (Technology, Accounting, Finance, Marketing, etc.), Time Interval (Periodic, One-time, Special), Function (Informational, Analytical, etc.), Physical Factors (Memorandum, Letter, Short & Long)	
	1.2	**Parts of a Long Formal Report** Prefatory Parts (Front Matter), Report Proper (Main Body), Appended Parts (Back Matter)	
	1.3	**Language and Style of Reports** Tense, Person & Voice of Reports, Numbering Style of Chapters, Sections, Figures, Tables and Equations, Referencing Styles in APA & MLA Format, Proof-reading through Plagiarism Checkers	
	1.4	**Definition, Purpose & Types of Proposals** Solicited (in conformance with RFP) & Unsolicited Proposals, Types (Short and Long proposals)	
	1.5	**Parts of a Proposal** Elements, Scope and Limitations, Conclusion	
	1.6	**Technical Paper Writing** Parts of a Technical Paper (Abstract, Introduction, Research Methods, Findings and Analysis, Discussion, Limitations, Future Scope and References), Language and Formatting, Referencing in IEEE Format	
2		**EMPLOYMENT SKILLS**	06
	2.1	**Cover Letter & Resume** Parts and Content of a Cover Letter, Difference between Bio-data, Resume & CV, Essential Parts of a Resume, Types of Resume (Chronological, Functional & Combination)	
	2.2	**Statement of Purpose** Importance of SOP, Tips for Writing an Effective SOP	
	2.3	**Verbal Aptitude Test** Modelled on CAT, GRE, GMAT exams	
	2.4	**Group Discussions** Purpose of a GD, Parameters of Evaluating a GD, Types of GDs (Normal, Case-based & Role Plays), GD Etiquette	
	2.5	**Personal Interviews** Planning and Preparation, Types of Questions, Types of Interviews (Structured, Stress, Behavioral, Problem Solving & Case-based), Modes of Interviews: Face-to-face (One-to one and Panel) Telephonic, Virtual	
3		**BUSINESS MEETINGS**	02
	3.1	**Conducting Business Meetings** Types of Meetings, Roles and Responsibilities of Chairperson, Secretary and Members, Meeting Etiquette	
	3.2	**Documentation** Notice, Agenda, Minutes	

4		**TECHNICAL/ BUSINESS PRESENTATIONS**	02
	4.1	**Effective Presentation Strategies** Defining Purpose, Analyzing Audience, Location and Event, Gathering, Selecting &Arranging Material, Structuring a Presentation, Making Effective Slides, Types of Presentations Aids, Closing a Presentation, Platform Skills	
	4.2	**Group Presentations** Sharing Responsibility in a Team, Building the contents and visuals together, Transition Phases	
5		**INTERPERSONAL SKILLS**	08
	5.1	**Interpersonal Skills** Emotional Intelligence, Leadership & Motivation, Conflict Management & Negotiation, Time Management, Assertiveness, Decision Making	
	5.2	**Start-up Skills** Financial Literacy, Risk Assessment, Data Analysis (e.g. Consumer Behavior, Market Trends, etc.)	
6		**CORPORATE ETHICS**	02
	6.1	**Intellectual Property Rights** Copyrights, Trademarks, Patents, Industrial Designs, Geographical Indications Integrated Circuits, Trade Secrets (Undisclosed Information)	
	6.2	**Case Studies** Cases related to Business/ Corporate Ethics	
		Total	**26**

LIST OF ASSIGNMENTS FOR TERMWORK:
(In the form of Short Notes, Questionnaire/ MCQ Test, Role Play, Case Study, Quiz, etc.)

1. Cover Letter and Resume
2. Short Proposal
3. Meeting Documentation
4. Writing a Technical Paper/ Analyzing a Published Technical Paper
5. Writing a SOP
6. IPR
7. Interpersonal Skills
8. Aptitude test (Verbal Ability)

Note:

1. *The Main Body of the project/book report should contain minimum 25 pages (excluding Front and Back matter).*
2. *The group size for the final report presentation should not be less than 5 students or exceed 7 students.*
3. *There will be an end–semester presentation based on the book report.*

GUIDELINES FOR INTERNAL ASSESSMENT

Term Work:

Term work shall consist of minimum 8 experiments.

The distribution of marks for term work shall be as follows:

Assignment	: 10 Marks
Attendance	: 5 Marks
Presentation slides	: 5 Marks
Book Report (hard copy)	: 5 Marks

The final certification and acceptance of term work ensures the satisfactory performance of laboratory work and minimum passing in the term work.

Internal oral:

Oral Examination will be based on a GD & the Project/Book Report presentation.

Group Discussion	:10 marks
Project Presentation	:10 Marks
Group Dynamics	:5 Marks

Text books and Reference books:

1. Arms, V. M. (2005). *Humanities for the engineering curriculum: With selected chapters from Olsen/Huckin: Technical writing and professional communication, second edition*. Boston, MA: McGraw-Hill.
2. Bovée, C. L., &Thill, J. V. (2021). *Business communication today*. Upper Saddle River, NJ: Pearson.
3. Butterfield, J. (2017). *Verbal communication: Soft skills for a digital workplace*. Boston, MA: Cengage Learning.
4. Masters, L. A., Wallace, H. R., & Harwood, L. (2011), *Personal development for life and work*. Mason: South-Western Cengage Learning.
5. Robbins, S. P., Judge, T. A., & Campbell, T. T. (2017). *Organizational behaviour*. Harlow, England: Pearson.
6. Meenakshi Raman, Sangeeta Sharma (2004) Technical Communication, Principles and Practice. Oxford University Press
7. Archana Ram (2018) Place Mentor, Tests of Aptitude For Placement Readiness. Oxford University Press
8. Sanjay Kumar &Pushp Lata (2018). Communication Skills a workbook, New Delhi: Oxford University Press.

Subject Code	Subject Name	Credits Assigned
ECM501	Mini project - 2A	02

Course Code	Course Name	Examination Scheme							
		Theory Marks					Term Work	Practical/ Oral	Total
		Internal Assessment			End Sem Exam	Exam duration Hours			
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ECM501	Mini project - 2A	--	--	--	--	--	25	25	50

Objectives

1. To acquaint with the process of identifying the needs and converting it into the problem.
2. To familiarize the process of solving the problem in a group.
3. To acquaint with the process of applying basic engineering fundamentals to attempt solutions to the problems.
4. To inculcate the process of self-learning and research.

Outcomes:

Learner will be able to;

1. Identify problems based on societal /research needs.
2. Apply knowledge and skill to solve societal problems in a group.
3. Develop interpersonal skills to work as member of a group or leader.
4. Draw the proper inferences from available results through theoretical/experimental/simulations.
5. Analyze the impact of solutions in societal and environmental context for sustainable development.
6. Use standard norms of engineering practices.
7. Excel in written and oral communication.
8. Demonstrate capabilities of self-learning in a group, which leads to life-long learning.
9. Demonstrate project management principles during project work.

Guidelines for Mini Project

- Students shall form a group of 3 to 4 students, while forming a group shall not be allowed less than three or more than four students, as it is a group activity.
- Students should do survey and identify needs, which shall be converted into problem statement for mini project in consultation with faculty supervisor/head of department/internal committee of faculties.
- Major focus of Mini-project 2 shall be towards exploration and applicability of

knowledge acquired in the domain areas of DLOs available for the year.

- Student shall give special consideration to identify and provide solutions to the burning societal and/or environmental issues which may affect the mankind to larger extend.
- Students shall submit implementation plan in the form of Gantt/PERT/CPM chart, which will cover weekly activity of mini project.

A log book to be prepared by each group, wherein group can record weekly work progress, guide/supervisor can verify and record notes/comments.

- Faculty supervisor may give inputs to students during mini project activity; however, focus shall be on
  self- learning.
- Students in a group shall understand problem effectively, propose multiple solution and select best possible solution in consultation with guide/supervisor.
- Students shall convert the best solution into working model using various components of their domain areas and demonstrate.
- The solution to be validated with proper justification and report to be compiled in standard format of University of Mumbai.
- With the focus on the self-learning, innovation, addressing societal problems and entrepreneurship quality development within the students through the Mini Projects, it is preferable that a single project of appropriate level and quality to be carried out in two semesters by all the groups of the students. i.e. Mini Project 1 in semester III and IV. Similarly, Mini Project 2 in semesters V and VI.
- However, based on the individual students or group capability, with the mentor's recommendations, if the proposed Mini Project adhering to the qualitative aspects mentioned above gets completed in odd semester, then that group can be allowed to work on the extension of the Mini Project with suitable improvements/modifications or a completely new project idea in even semester. This policy can be adopted on case-to-case basis.

Guidelines for Assessment of Mini Project:

The review/ progress monitoring committee shall be constituted by head of departments of each institute. The progress of mini project to be evaluated on continuous basis, minimum two reviews in each semester. In continuous assessment focus shall also be on each individual student, assessment based on individual's contribution in group activity, their understanding and response to questions.

Distribution of Term work marks for both semesters shall be as below;

Marks awarded by guide/supervisor based on logbook:		***10***
Marks awarded by review committee	***:***	***10***
Quality of Project report	***:***	***05***

Review/progress monitoring committee may consider following points for assessment based on either one year or half year project as mentioned in general guidelines.

One-year project:

In **first semester** entire theoretical solution shall be ready, including components/system selection and cost analysis. Two reviews will be conducted based on presentation given by students group.

- First on identification and finalization of problem
- Second on proposed solution for the problem.

In **second semester** expected work shall be procurement of component's/systems, building of working prototype, testing and validation of results based on work completed in an earlier semester.

- First review shall base on readiness of building working prototype.
- Second review shall be based on poster presentation-cum-demonstration of working model in last month of the said semester.

Half-year project:

In this case students' group shall complete project in all aspects, in a semester, including;

- Identification of need/problem
- Proposed acceptable solution for the identified problem
- Procurement of components/systems, if any,
- Building a working prototype and testing

The group shall be evaluated twice during the semester by review committee, mainly look for the progress as;

- First review focus shall be towards identification & selection of problem and probable solution proposal.
- Second review shall be for implementation and testing of solution. (Innovative/out of box solution)

Assessment criteria of Mini Project.

Mini Project shall be assessed based on following criteria:

1. Quality of survey/ need identification
2. Clarity of Problem definition based on need.
3. Innovativeness in solutions
4. Feasibility of proposed problem solutions and selection of best solution
5. Innovativeness and out of box thinking
6. Cost effectiveness and Societal impact
7. Functional working model as per stated requirements
8. Effective use of skillsets acquired through curriculum including DLOs
9. Effective use of standard engineering practices & norms
10. Contribution of an individual as team member/Leader
11. Feasibility to deploy the solution on large scale
12. Clarity in written and oral communication

In **one year, project**, first semester evaluation may be based on first six criteria's and remaining may be used for second semester evaluation of performance of students in mini-project.
In case of **half year project** all criteria's in generic may be considered for performance evaluation of students in mini-project.

Guidelines for Assessment of Mini Project Practical/Oral Examination:

Report should be prepared as per the guidelines issued by the University of Mumbai. Mini Project shall be assessed through a presentation and demonstration of working model by the student project group to a panel of Internal and External Examiners preferably from industry or research organizations, having experience of more than five years approved by head of the Institute.

Students shall be motivated to publish a paper based on the work in Conferences/students competitions.

Mini Project shall be assessed by team of external & internal examiner at the end of semester/year. Performance shall be evaluated based on;

1. Quality of problem and Clarity
2. Innovativeness in solutions
3. Cost effectiveness and Societal impact
4. Implementation of working model
5. Effective use of diversified skill-set
6. Effective use of standard engineering practices & norms
7. Contribution of an individuals as a member/Leader
8. Clarity in written and oral communication